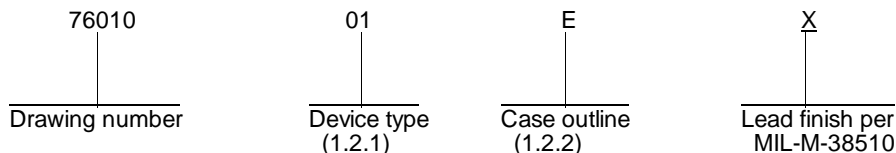


REVISIONS																			
LTR	DESCRIPTION										DATE (YR-MO-DA)					APPROVED			
E	Remove vendors CAGE 18324, 27014 and 34335. Convert to Military drawing format. Editorial changes throughout. Add LCC package.										19 JUNE 1987					N.A. Hauck			
<div>Device 01 inactive for new design. Use QPL-38510 product.</div>																			
REV																			
SHEET																			
REV																			
SHEET																			
REV STATUS OF SHEETS				REV		E	E	E	E	E	E	E	E	E	E	E	E	E	
				SHEET		1	2	3	4	5	6	7	8	9	10	11	12	13	
PMIC N/A				PREPARED BY JOE KERBY						DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 43216									
<div>MILITARY DRAWING</div> <div>THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE</div> <div>AMSC N/A</div>				CHECKED BY D. A. DICENZO															
				APPROVED BY N. A. HAUCK															
				DRAWING APPROVAL DATE 25 OCTOBER 1977															
				REVISION LEVEL E															
								SIZE		CAGE CODE			76010						
				A		14933													
				SHEET		1		OF		13									

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit</u>
01	54LS151	8-line to 1-line data selector/multiplexer

1.2.2 Case outline. The case outline shall be as designated in MIL-M-38510, appendix C and as follows:

<u>Outline letter</u>	<u>Case outline</u>
E	D-2 (16-lead, 1/4" x 7/8"), dual-in-line package
F	F-5 (16-lead, 1/4" x 3/8"), flat package
2	C-2 (20-terminal .350" x .350"), square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage range -----	-0.5 V dc to +7.0 V dc
Input voltage range -----	-1.5 V dc at -18 mA to +5.5 V dc
Storage temperature range-----	-65° C to +150° C
Maximum power dissipation (P_D) per device <u>1</u> /-----	55 mW
Lead temperature (soldering 10 seconds) -----	+300° C
Thermal resistance, junction to case (θ_{JC}):	
Cases E and F -----	(See MIL-M-38510, appendix C)
Case 2 -----	80° C/W <u>2</u> /
Junction temperature (T_J)------	+175° C

1.4 Recommended operating conditions.

Supply voltage (V_{CC})------	4.5 V dc minimum to 5.5 V dc maximum
Minimum high-level input voltage (V_{IH}) -----	2.0 V dc
Maximum low-level input voltage (V_{IL}) -----	0.7 V dc
Case operating temperature range (T_C) -----	-55° C to +125° C

1/ Must withstand the added P_D due to short-circuit test (e.g., I_{OS}).

2/ When a thermal resistance for this case is specified in MIL-M-38510, appendix C, that value shall supersede the value specified herein.

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2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Logic diagram. The logic diagram shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended case operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _C ≤ +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
High-level output voltage	V _{OH}	V _{CC} = 4.5 V; I _{OH} = -400 μA; V _{IN} = 0.7 V or 2.0 V	1,2,3	2.5		V
Low-level output voltage	V _{OL}	V _{CC} = 4.5 V; I _{OL} = 4 mA; V _{IN} = 0.7 V or 2.0 V	1,2,3		0.4	V
Input clamp voltage	V _{IC}	V _{CC} = 4.5 V; I _{IN} = -18 mA; T _C = +25°C	1		-1.5	V
High-level input current	I _{IH1}	V _{CC} = 5.5 V; V _{IH} = 2.7 V	1,2,3		20	μA
	I _{IH2}	V _{CC} = 5.5 V; V _{IH} = 5.5 V	1,2,3		100	μA
Low-level input current	I _{IL}	V _{CC} = 5.5 V; V _{IL} = 0.4 V	1,2,3		-400	μA
Short-circuit output current	I _{OS}	V _{CC} = 5.5 V; V _{OUT} = 0.0 V 1/	1,2,3	-6	-130	mA
Supply current	I _{CC}	V _{CC} = 5.5 V	1,2,3		10	mA
Functional tests		See 4.3.1c	7			
Propagation delay time, high-to-low-level A, B or C to Y 4 levels 2/	t _{PHL1}	V _{CC} = 5.0 V R _L = 2 kΩ ±5%	C _L = 15 pF ±10%	9	30	ns
				10,11	42	ns
			C _L = 50 pF ±10%	9	35	ns
				10,11	49	ns

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T _C ≤ +125°C unless otherwise specified		Group A subgroups	Limits		Unit
					Min	Max	
Propagation delay time, low-to-high-level A, B or C to Y 4 levels <u>2</u> /	t _{PLH1}	V _{CC} = 5.0 V R _L = 2 kΩ ±5%	C _L = 15 pF ±10%	9		43	ns
				10,11		60	ns
			C _L = 50 pF ±10%	9		48	ns
				10,11		67	ns
Propagation delay time, high-to-low-level A, B or C to W 3 levels <u>2</u> /	t _{PHL2}		C _L = 15 pF ±10%	9		32	ns
				10,11		45	ns
			C _L = 50 pF ±10%	9		37	ns
				10,11		52	ns
Propagation delay time, low-to-high-level A, B or C to W 3 levels <u>2</u> /	t _{PLH2}	C _L = 15 pF ±10%	9		23	ns	
			10,11		32	ns	
		C _L = 50 pF ±10%	9		28	ns	
			10,11		39	ns	
Propagation delay time, high-to-low-level Strobe to Y <u>2</u> /	t _{PHL3}	C _L = 15 pF ±10%	9		32	ns	
			10,11		45	ns	
		C _L = 50 pF ±10%	9		37	ns	
			10,11		52	ns	

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$ unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Propagation delay time, low-to-high-level Strobe to Y <u>2</u> /	t_{PLH3}	$V_{CC} = 5.0\text{ V}$ $R_L = 2\text{ k}\Omega \pm 5\%$	$C_L = 15\text{ pF} \pm 10\%$	9	42	ns
				10,11	59	ns
			$C_L = 50\text{ pF} \pm 10\%$	9	47	ns
				10,11	66	ns
			$C_L = 15\text{ pF} \pm 10\%$	9	30	ns
				10,11	42	ns
Propagation delay time, high-to-low-level Strobe to W <u>2</u> /	t_{PHL4}		$C_L = 50\text{ pF} \pm 10\%$	9	35	ns
				10,11	49	ns
			$C_L = 15\text{ pF} \pm 10\%$	9	24	ns
				10,11	34	ns
			$C_L = 50\text{ pF} \pm 10\%$	9	29	ns
				10,11	41	ns
Propagation delay time, low-to-high-level Strobe to W <u>2</u> /	t_{PLH4}		$C_L = 15\text{ pF} \pm 10\%$	9	26	ns
				10,11	36	ns
			$C_L = 50\text{ pF} \pm 10\%$	9	31	ns
				10,11	43	ns
			$C_L = 15\text{ pF} \pm 10\%$	9	32	ns
				10,11	45	ns
Propagation delay time, high-to-low-level DO-D7 to Y <u>2</u> /	t_{PHL5}		$C_L = 50\text{ pF} \pm 10\%$	9	37	ns
				10,11	52	ns
			$C_L = 15\text{ pF} \pm 10\%$	9	32	ns
				10,11	45	ns
			$C_L = 50\text{ pF} \pm 10\%$	9	37	ns
				10,11	52	ns

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$ unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Propagation delay time, high-to-low-level DO-D7 to W <u>2/</u>	t_{PHL6}	$V_{\text{CC}} = 5.0\text{ V}$ $R_L = 2\text{ k}\Omega \pm 5\%$	$C_L = 15\text{ pF} \pm 10\%$	9	20	ns
				10,11	28	ns
			$C_L = 50\text{ pF} \pm 10\%$	9	21	ns
				10,11	29	ns
Propagation delay time, low-to-high-level DO-D7 to W <u>2/</u>	t_{PLH6}		$C_L = 15\text{ pF} \pm 10\%$	9	21	ns
				10,11	29	ns
			$C_L = 50\text{ pF} \pm 10\%$	9	26	ns
				10,11	36	ns

- 1/ Not more than one output should be shorted at a time, and the duration of the short-circuit condition should not exceed one second.
- 2/ Propagation delay time testing may be performed using either $C_L = 15\text{ pF}$ or $C_L = 50\text{ pF}$. However, the manufacturer must certify and guarantee that the microcircuits meet the switching test limits specified for a 50-pF load.

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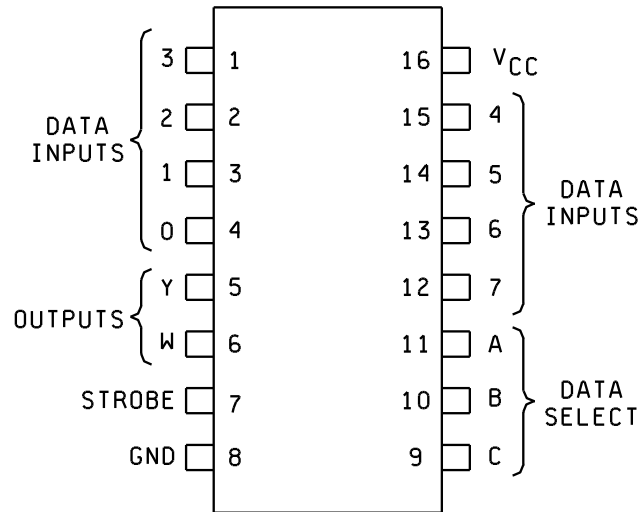
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CASES E AND F



CASE 2

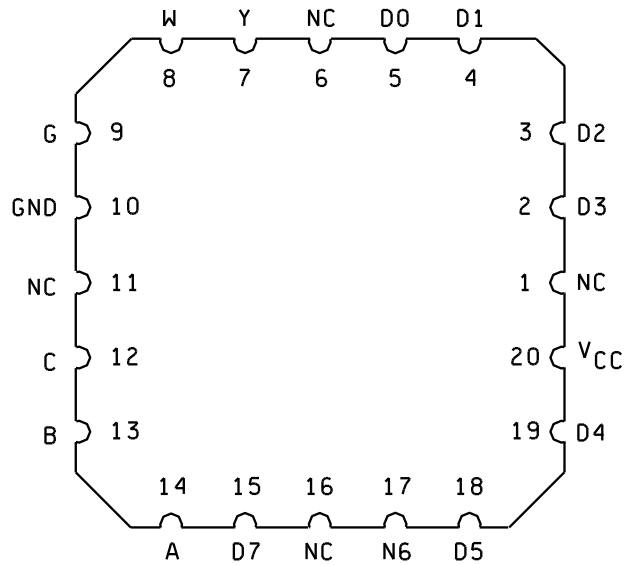


FIGURE 1. Terminal connections.

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Inputs				Outputs	
Select			Strobe		
C	B	A	S	Y	W
X	X	X	H	L	H
L	L	L	L	D0	$\overline{D0}$
L	L	H	L	D1	$\overline{D1}$
L	H	L	L	D2	$\overline{D2}$
L	H	H	L	D3	$\overline{D3}$
H	L	L	L	D4	$\overline{D4}$
H	L	H	L	D5	$\overline{D5}$
H	H	L	L	D6	$\overline{D6}$
H	H	H	L	D7	$\overline{D7}$

H = high level, L = low level, X = irrelevant
D0, D1 ... D7 = the level of the respective inputs.

FIGURE 2. Truth table.

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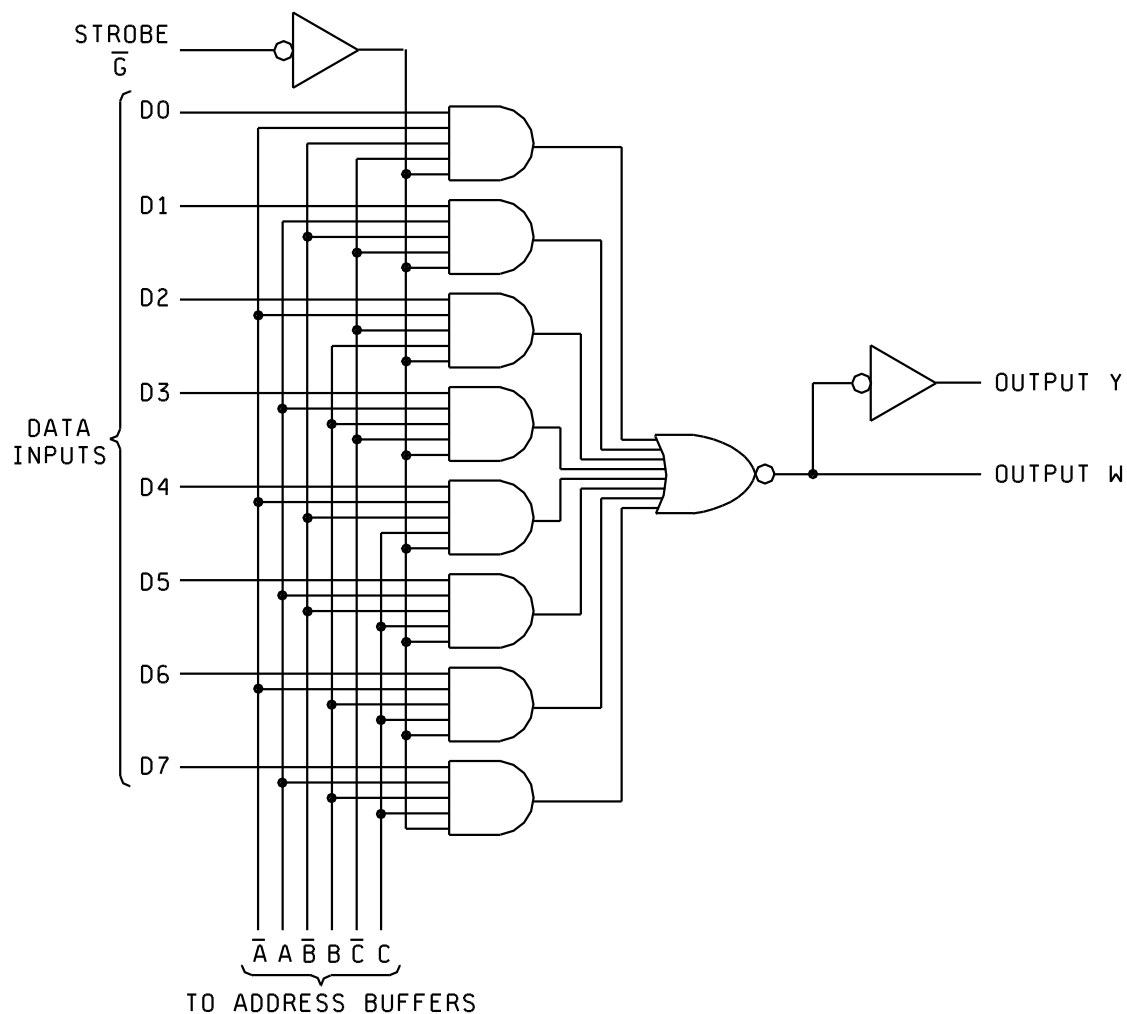


FIGURE 3. Logic diagram.

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3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test (method 1015 of MIL-STD-883).

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2) $T_A = +125^{\circ}\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroup 7 tests shall verify the truth table.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test (method 1005 of MIL-STD-883) conditions:

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2) $T_A = +125^{\circ}\text{C}$, minimum.

(3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*, 2, 3, 9
Group A test requirements (method 5005)	1, 2, 3, 7, 9, 10, 11**
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

* PDA applies to subgroup 1.

** Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Replaceability is determined as follows:

- a. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- b. When a QPL source is established, the part numbered device specified in this drawing will be replaced by the microcircuit identified as part number M38510/30901B--.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved sources of supply. Approved sources of supply are listed herein. Additional sources will be added as they become available. The vendors listed herein have agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1/</u>	Replacement military specification part number
7601001EX <u>2/</u>	01295 07263 04713	SNJ54LS151J 54LS151DMQB 54LS151BEXJC	M38510/30901BEX
7601001FX <u>2/</u>	01295 07263 04713	SNJ54LS151W 54LS151FMQB 54LS151BFXJC	M38510/30901BFX
76010012X <u>2/</u>	01295 07263 04713	SNJ54LS151J 54LS151LMQB 54LS151M/B2CJC	M38510/30901B2X

1/ **CAUTION.** Do not use this number for item acquisition. Items acquired to the vendor similar part number only may not satisfy the performance requirements of this drawing.

2/ Inactive for new design. Use QPL-38510 product.

Vendor CAGE number

01295

07263

04713

Vendor name and address

Texas Instruments, Inc.
P.O. Box 6448
Midland, TX 79701

Fairchild Semiconductor
333 Western Avenue
South Portland, ME 04106

Motorola, Inc.
7402 S. Price Road
Tempe, AZ 85283

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